**What Is Claimed Is:**

1. A flat panel display allowing repair of data line defects, comprising:

a plurality of gate lines and a plurality of data lines intersecting to define a plurality of pixel regions;

a plurality of pixel electrodes disposed in the pixel regions respectively;

a plurality of first shielding metal lines disposed between the pixel electrodes and the data lines in the pixel regions respectively, each first shielding metal line separated from the others; and

at least two first backup wires across the adjacent first shielding metal line and data line, wherein the first backup wires are isolated from the data lines or the first shielding metal lines or isolated from both of them.

2. The flat panel display of claim 1, wherein the first backup wires extend from the data lines.

3. The flat panel display of claim 2, wherein a plurality of overlap points of the first backup wires and the first shielding metal lines act as a plurality of repair points.

4. The flat panel display of claim 2, wherein the first shielding metal lines and the gate lines are in a first metal layer, and the first backup wires and the data lines are in a second metal layer.

5. The flat panel display of claim 2, wherein the first backup wires and the data lines are in a first metal layer, and the first shielding metal lines and the gate lines are in a second metal layer.

6. The flat panel display of claim 1, wherein the first backup wires extend from the first shielding metal lines.

7. The flat panel display of claim 6, wherein a plurality of overlap points of the first backup wires and the data lines act as a plurality of repair points.

8. The flat panel display of claim 6, wherein the first backup wires, the first shielding metal lines and the gate lines are in a first metal layer, and the data lines are in a second metal layer.

9. The flat panel display of claim 6, wherein the data lines are in a first metal layer, and the first backup wires, the first shielding metal lines and the gate lines are in a second metal layer.

10. The flat panel display of claim 1, wherein the first backup wires are isolated from the data lines and the first shielding metal layer.

11. The flat panel display of claim 10, wherein a plurality of overlap points of the first backup wires, the data lines, and the first shielding metal lines act as a plurality of repair points.

12. The flat panel display of claim 10, wherein the first backup wires and the gate lines are in a first metal layer, and the first shielding metal layer and the data lines are in a second metal layer.

13. The flat panel display of claim 10, wherein the first shielding metal layer and the data lines are in a first metal layer, and the first backup wires and the gate lines are in a second metal layer.

14. The flat panel display of claim 1, further comprising:
a plurality of second shielding metal lines disposed between the pixel electrodes and the gate lines in the pixel regions

respectively, each first shielding metal line separated from the others; and

at least two second backup wires across the adjacent second shielding metal line and gate line, wherein the second backup wires are isolated from the gate lines or the second shielding metal lines or isolated from both of them.

15. The flat panel display of claim 14, wherein the second backup wires extend from the gate lines.

16. The flat panel display of claim 15, wherein a plurality of overlap points of the second backup wires and the second shielding metal lines act as a plurality of repair points.

17. The flat panel display of claim 14, wherein the second backup wires extend from the second shielding metal lines.

18. The flat panel display of claim 17, wherein a plurality of overlap points of the second backup wires and the gate lines act as a plurality of repair points.

19. The flat panel display of claim 14, wherein the second backup wires are isolated from the gate lines and the second shielding metal layer.

20. The flat panel display of claim 19, wherein a plurality of overlap points of the second backup wires, the gate lines, and the second shielding metal lines act as a plurality of repair points.

21. A repair method for a flat panel display allowing repair of data line defects, comprising:

providing a flat panel display including

a plurality of gate lines and a plurality of data lines intersecting to define a plurality of pixel regions;

a plurality of pixel electrodes disposed in the pixel regions respectively;

a plurality of first shielding metal lines disposed between the pixel electrodes and the data lines in the pixel regions respectively, each first shielding metal line separated from the others; and

at least two first backup wires across the adjacent first shielding metal line and data line, wherein the first backup wires are isolated from the data lines or the first shielding metal lines or isolated from both of them; and

in the event of a defect being detected in a first data line, one of the first shielding metal lines adjacent to the defect being selected, a pair of first backup wires on both sides of the defect being electrically connected to the first data line and the selected first shielding metal line.

22. A repair method for a flat panel display allowing repair of data line defects, comprising:

providing a flat panel display including

a plurality of gate lines and a plurality of data lines intersecting to define a plurality of pixel regions;

a plurality of pixel electrodes disposed in the pixel regions respectively;

a plurality of first shielding metal lines disposed between the pixel electrodes and the data lines in the pixel regions respectively, each first shielding metal line separated from the others;

at least two first backup wires across the adjacent first shielding metal line and data line, wherein the first backup wires are isolated from the data lines or the

first shielding metal lines or isolated from both of them;

a plurality of second shielding metal lines disposed between the pixel electrodes and the gate lines in the pixel regions respectively, each first shielding metal line separated from the others; and

at least two second backup wires across the adjacent second shielding metal line and gate line, wherein the second backup wires are isolated from the gate lines or the second shielding metal lines or isolated from both of them;

in the event of a first defect being detected in a first data line, one of the first shielding metal lines adjacent to the first defect is selected, with a pair of first backup wires on both sides of the first defect electrically connected to the first data line and the selected first shielding metal line; and

in the event of a second defect being detected in a first gate line, one of the second shielding metal lines adjacent to the second defect is selected, with a pair of second backup wires on both sides of the second defect electrically connected to the first gate line and the selected second shielding metal line.